SOMMAIRE : DATA BOOK CMOS

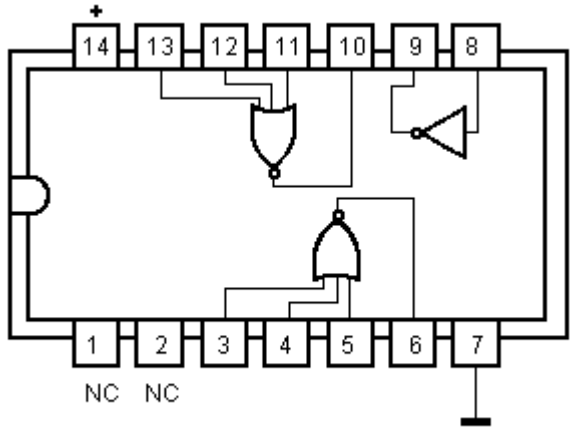
1. 3. - INVENTAIRE DES CIRCUITS DE LOGIQUE : FAMILLE C.MOS

• HEF 4000	• Double porte NON - OU à 3 entrées + inverseur
• 4001	• Quadruple porte NON - OU à 2 entrées
• 4002	• Double porte NON - OU à 4 entrées
• 4006	• Registre à décalage statique 18 étages
• 4007	• Double paire complémentaire + inverseur
• 4008	• Additionneur 4 bits avec retenue
• 4011	• Quadruple porte NON - ET
• 4012	• Double porte NON - ET à 4 entrées
• 4013	• Double bascule D
• 4014	• Registre à décalage 8 bits
• 4015	• Double registre à décalage 4 bits
• 4016	• Quadruple interrupteur bidirectionnel
• 4017	• Compteur Johnson à 5 étages
• 4018	• Compteur / diviseur par 'n' programmable
• 4019	• Quadruple multiplexeur à 2 entrées
• 4020	• Compteur binaire à 14 étages
• 4021	• Registre à décalage 8 bits
• 4022	• Compteur Johnson à 4 étages, diviseur par 8
• 4023	• Triple porte NON - ET à 3 entrées
• 4024	• Compteur binaire à 7 étages
• 4025	• Triple porte NON - OU à 3 entrées
• 4027	• Double bascule J - K
• 4028	• Décodeur BCD - décimal (1 parmi 10)
• 4029	• Compteur / décompteur synchrone, binaire / décimal
• 4030	• Quadruple porte OU - EXCLUSIF
• 4031	• Registre à décalage 64 bits
• 4035	• Registre à décalage universel 4 bits
• 4040	• Compteur binaire à 12 étages
• 4041	• Quadruple porte de puissance avec sortie complémentaire
• 4042	• Quadruple bascule D à verrouillage
• 4043	• Quadruple bascule NON - OU - R / S à verrouillage (sorties 3 états)
• 4044	• Quadruple bascule NON - ET - R / S à verrouillage (sorties 3 états)
• 4046	• Boucle à phase asservie (PLL)
• 4047	• Monostable
• 4049	• Sextuple porte de puissance (inverseur)
• 4050	• Sextuple porte de puissance (non - inverseur)

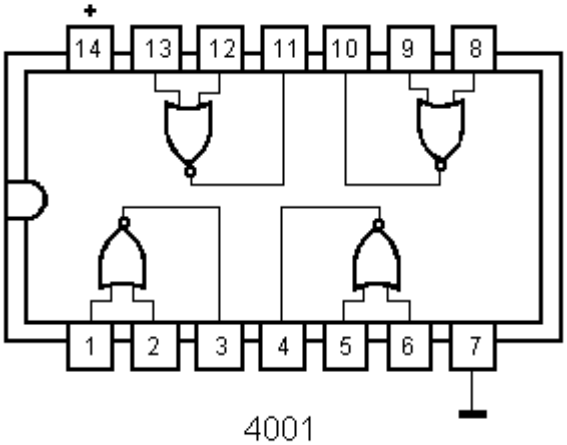
• 4051	• Multiplexeur / démultiplexeur analogique à 8 canaux
• 4052	• Double multiplexeur / démultiplexeur analogique à 4 canaux
• 4053	• Triple multiplexeur / démultiplexeur analogique à 2 canaux
• 4054	• Driver pour afficheur 4 segments LCD
• 4056	• Décodeur BCD pour afficheur 7 segments LCD
• 4060	• Un compteur-diviseur 14 étages avec oscillateur
• 4066	• Quadruple interrupteur bidirectionnel
• 4067	• Multiplexeur 16 - 1
• 4068	• Porte NON - ET à 8 entrées
• 4069	• Sextuple inverseur
• 4070	• Quadruple porte OU - EXCLUSIF
• 4071	• Quadruple porte OU à 2 entrées
• 4072	• Double porte OU à 4 entrées
• 4073	• Triple porte ET à 3 entrées
• 4075	• Triple porte OU à 3 entrées
• 4078	• Porte NON - OU à 8 entrées
• 4081	• Quadruple porte ET à 2 entrées
• 4082	• Double porte ET à 4 entrées
• 4085	• Double porte ET - OU - NON à 2 x 2 entrées
• 4086	• Porte ET - OU - NON à 4 x 2 entrées
• 4093	• Quadruple trigger de Schmitt NAND à 2 entrées
• 4098	• Double monostable redéclenchable
• 4104	• Quadruple translateur de tension à sortie 3 états
• 4510	• Compteur / décompteur BCD
• 4511	• Décodeur / driver 7 segments
• 4514	• Décodeur / démultiplexeur 1 parmi 16, avec registre d'entrée (sortie haute)
• 4515	• Décodeur / démultiplexeur 1 parmi 16, avec registre d'entrée (sortie basse)
• 4516	• Compteur / décompteur binaire
• 4518	• Double compteur décimal
• 4519	• Quadruple multiplexeur à 2 entrées
• 4520	• Double compteur binaire
• 4528	• Double monostable redéclenchable
• 4539	• Double multiplexeur à 4 entrées
• 4555	• Double décodeur / démultiplexeur 1 parmi 4 (sortie haute)
• 4556	• Double décodeur / démultiplexeur 1 parmi 4 (sortie basse)
• 4720	• Mémoire vive 256 bits (256 x 1)

• 4721	• Mémoire vive 1 024 bits (256 x 4)
• 4724	• Registre 8 bits adressable à verrouillage
• 4736	• Mémoire vive 1 024 (1 024 x 1)
• 40097	• Sextuple porte de puissance, sortie 3 états (non inverseur)
• 40098	• Sextuple porte de puissance, sortie 3 états (inverseur)
• 40106	• Six triggers de Schmitt inverseurs
• 40174	• Sextuple bascule D
• 40175	• Quadruple bascule D
• 40192	• Compteur / décompteur synchrone 4 bits décimal
• 40193	• Compteur / décompteur synchrone 4 bits binaire
• 40194	• Registre à décalage universel bidirectionnel 4 bits
• 40195	• Registre à décalage universel 4 bits

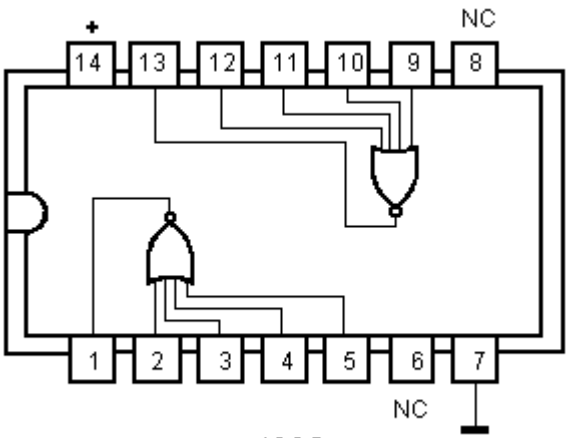
1. 4. - BROCHAGE DES CIRCUITS DE LOGIQUE : FAMILLE C.MOS



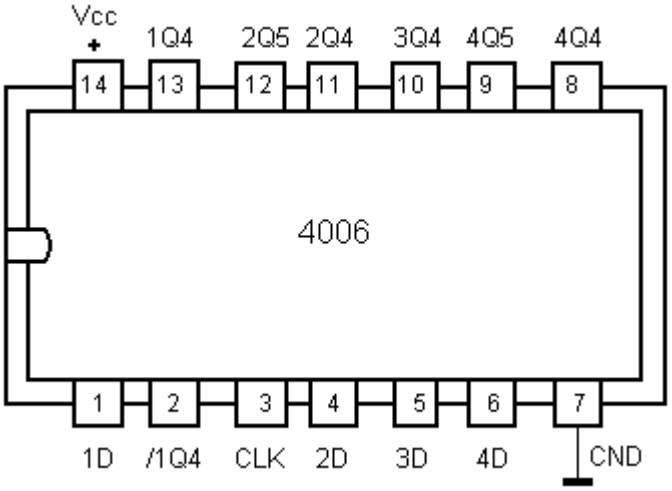
4000

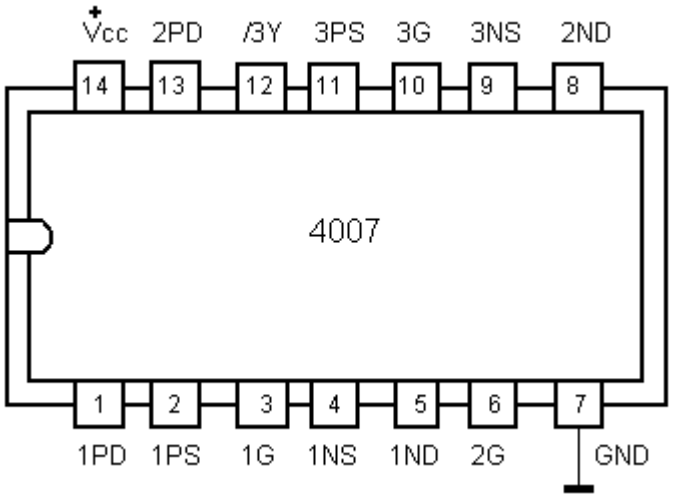


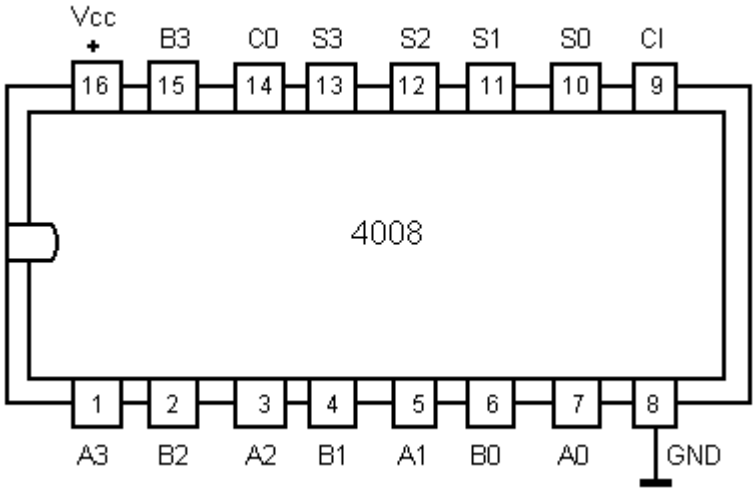
4001

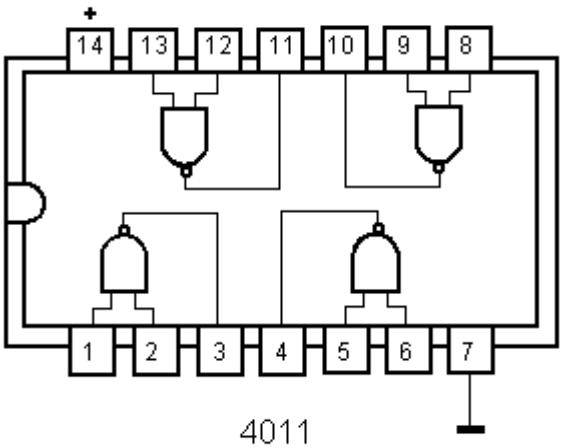


4002

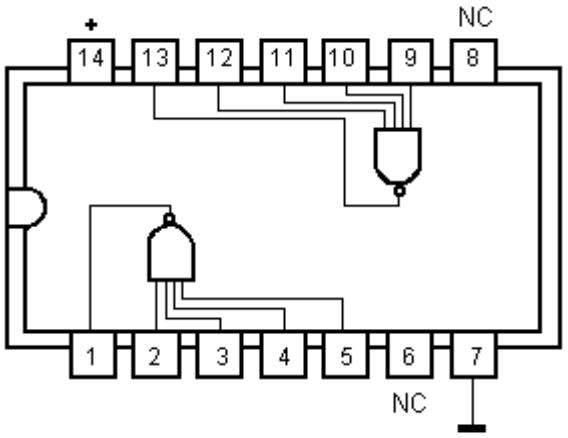



Vcc 2PD /3Y 3PS 3G 3NS 2ND
14 13 12 11 10 9 8
4007
1 2 3 4 5 6 7
1PD 1PS 1G 1NS 1ND 2G GND

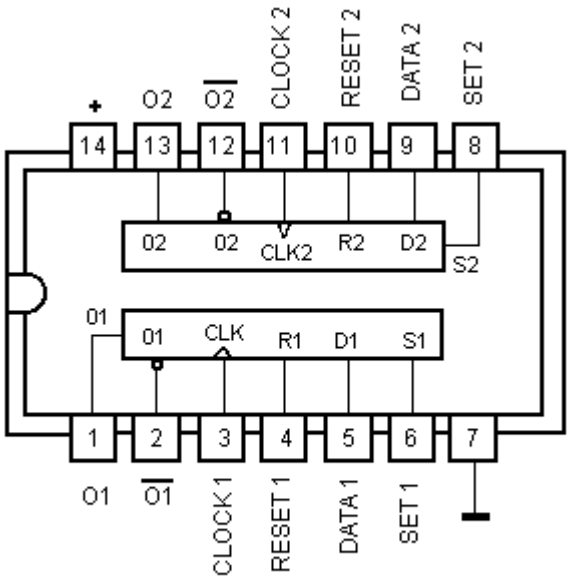

Vcc B3 C0 S3 S2 S1 S0 CI
16 15 14 13 12 11 10 9
4008
1 2 3 4 5 6 7 8
A3 B2 A2 B1 A1 B0 A0 GND


14 13 12 11 10 9 8
1 2 3 4 5 6 7

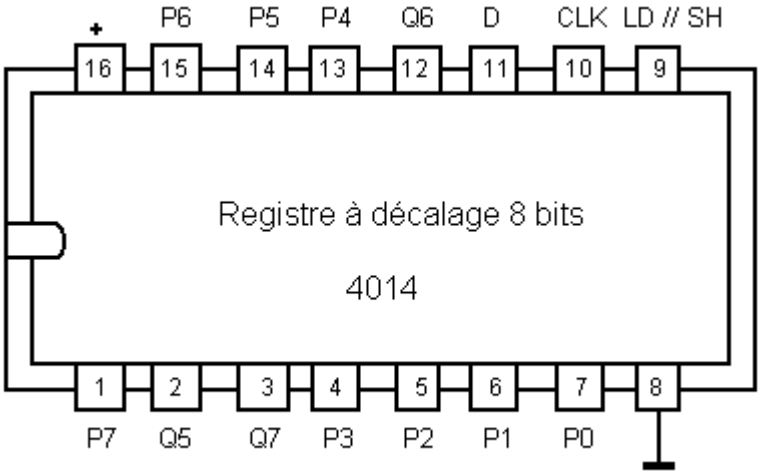
4011


NC
14 13 12 11 10 9 8
1 2 3 4 5 6 7
NC

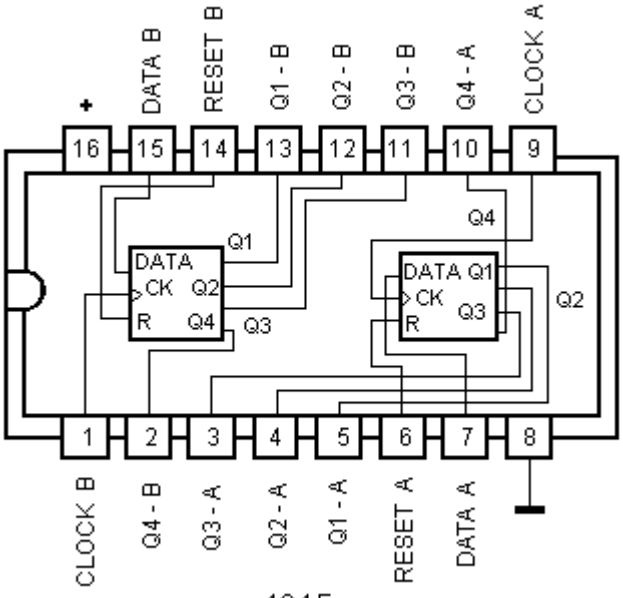
4012



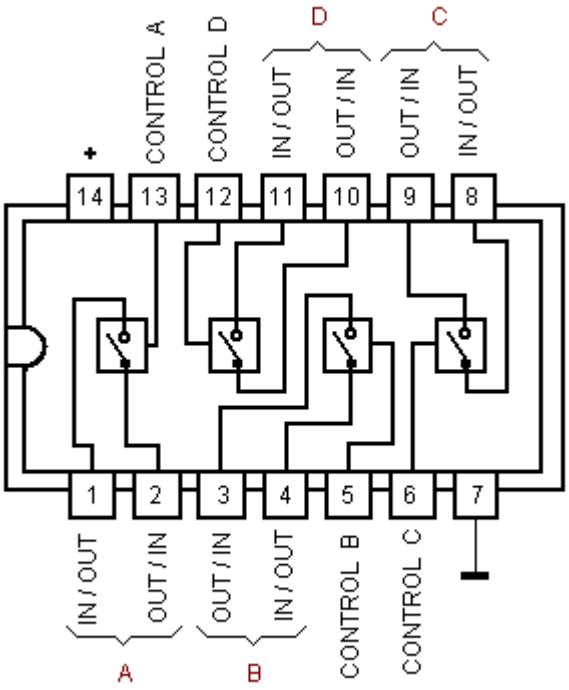
4013



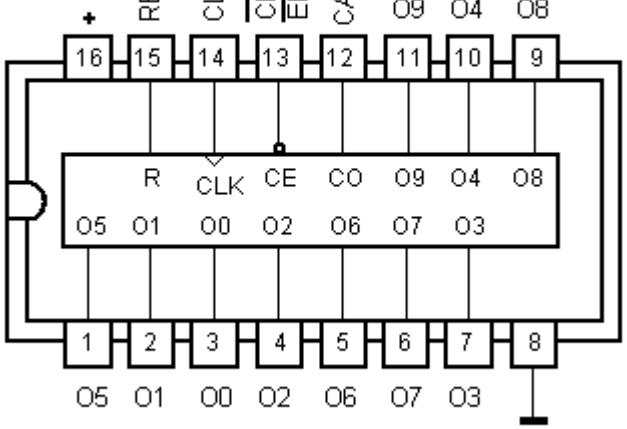
4014



4015

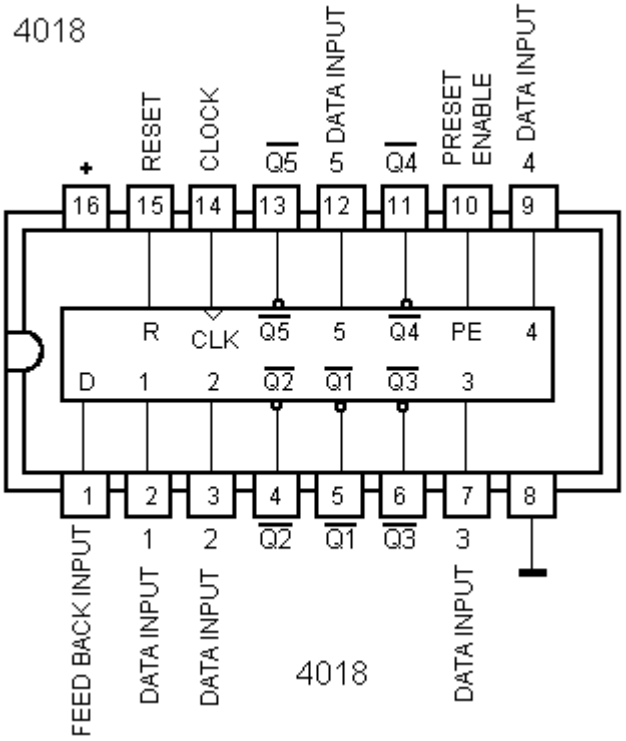


4016 - 4066

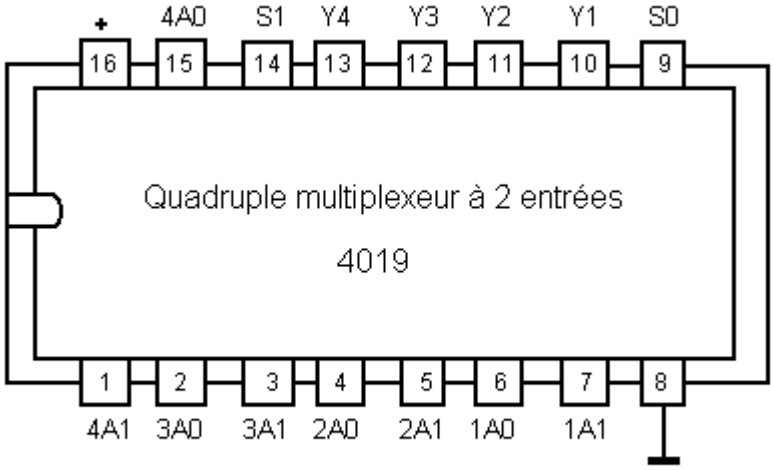


4017

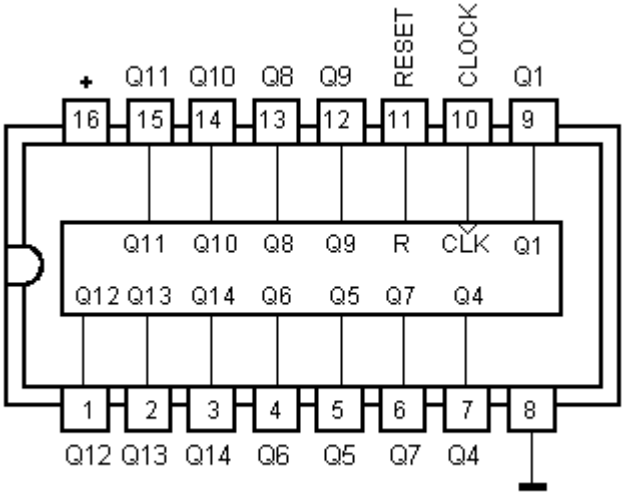
4018



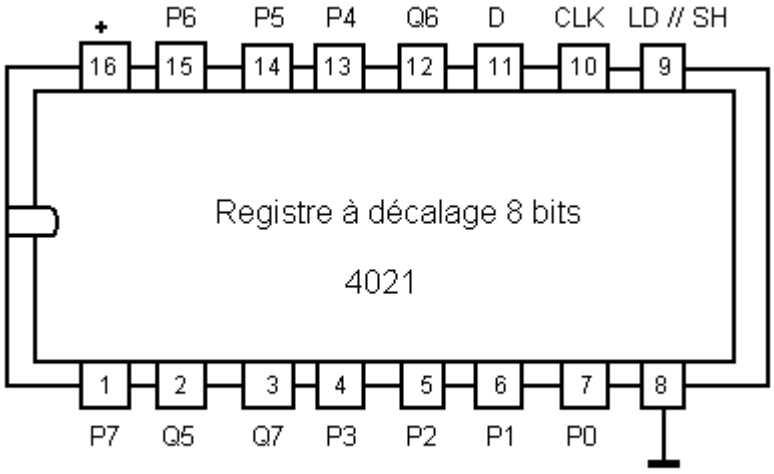
4018



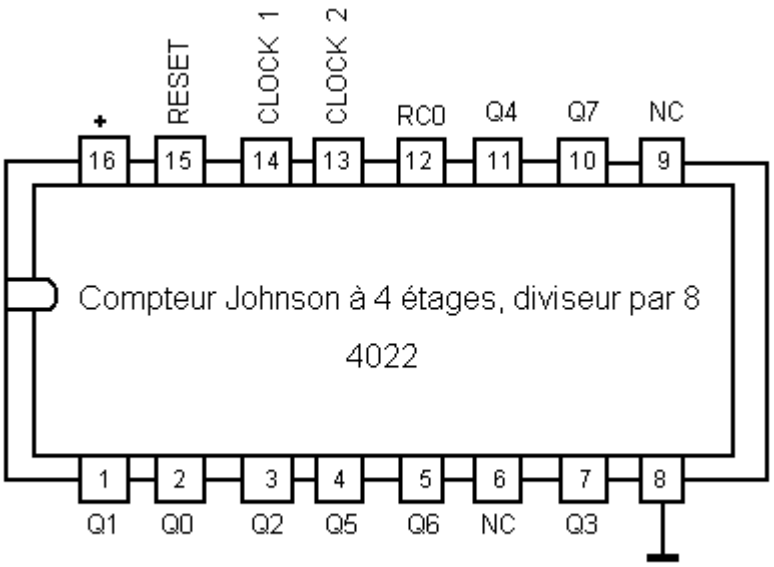
4019



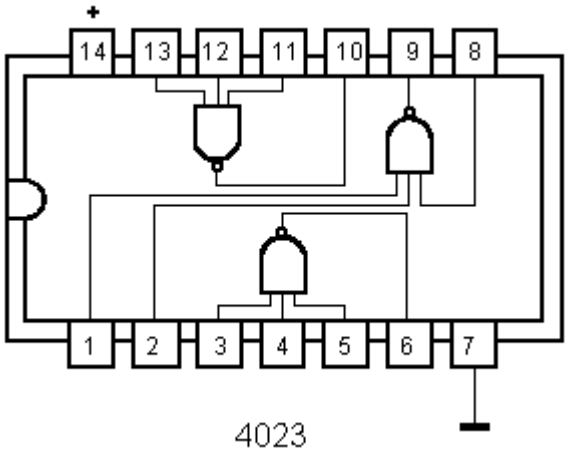
4020



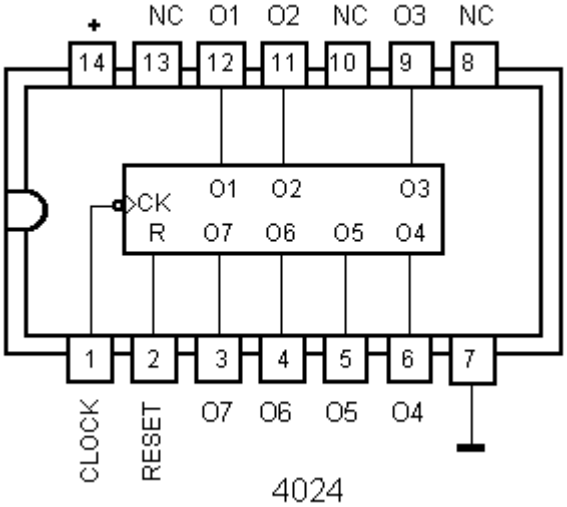
4021



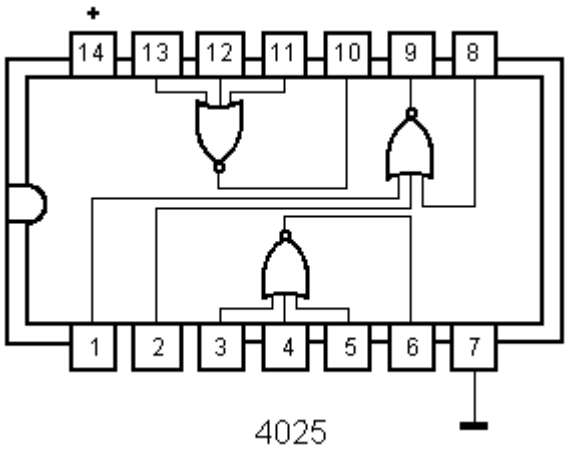
4022



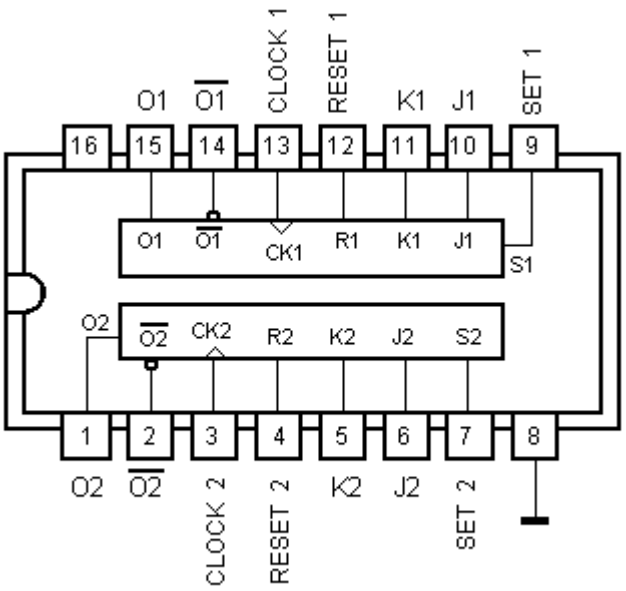
4023



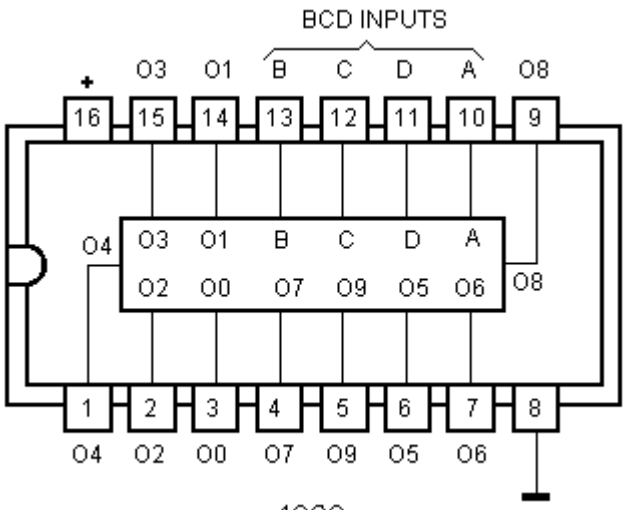
4024



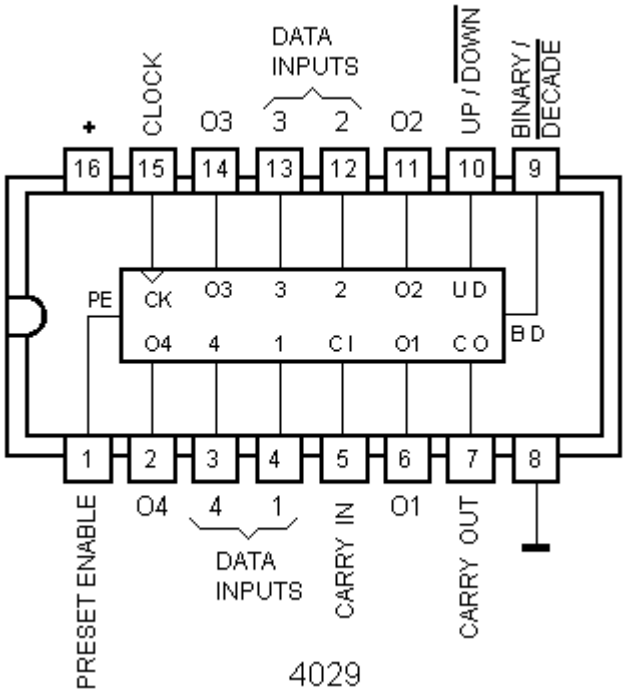
4025



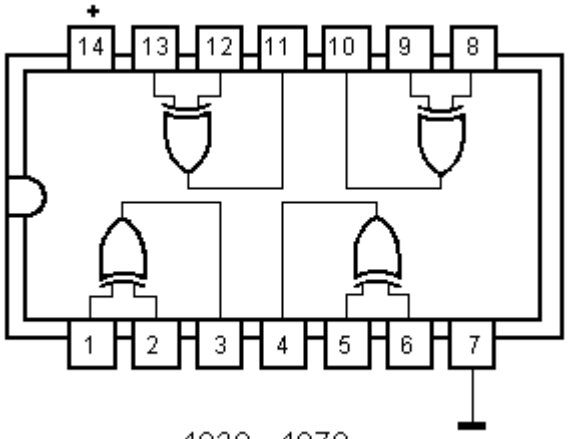
4027



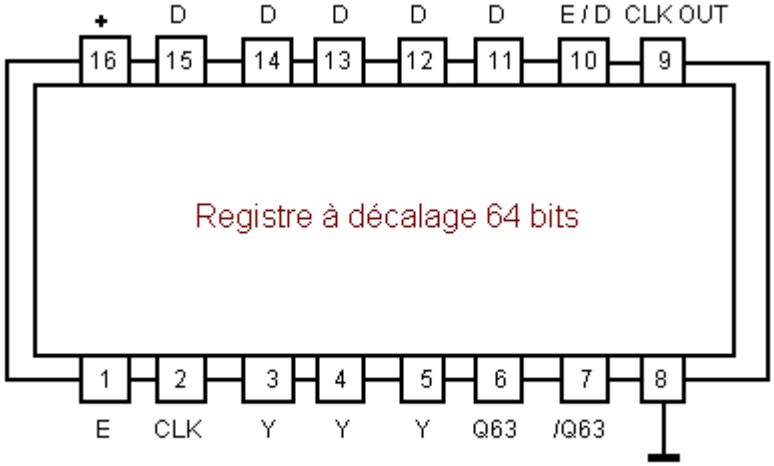
4028


DATA INPUTS
CLOCK O3 3 2 O2 UP / DOWN BINARY / DECADE
+
16 15 14 13 12 11 10 9
PE CK O3 3 2 O2 U D
O4 4 1 C I O1 C O B D
1 2 3 4 5 6 7 8
PRESET ENABLE O4 4 1 DATA INPUTS CARRY IN O1 CARRY OUT

4029

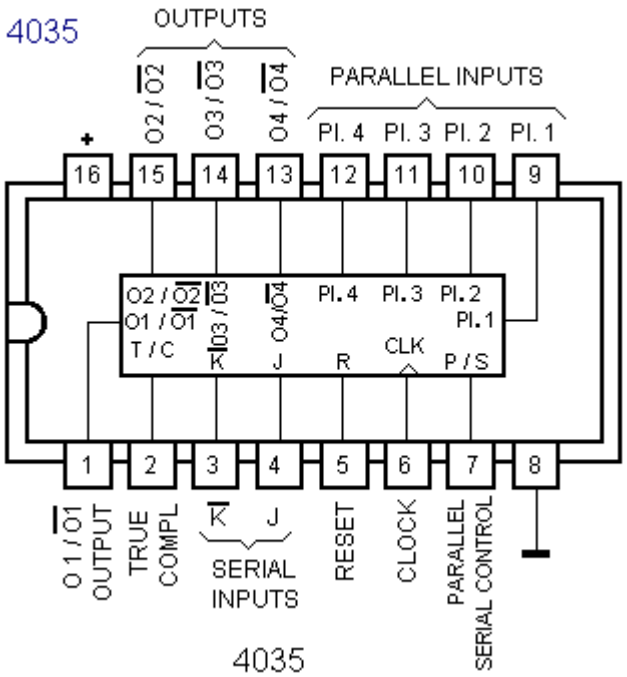


4030 - 4070



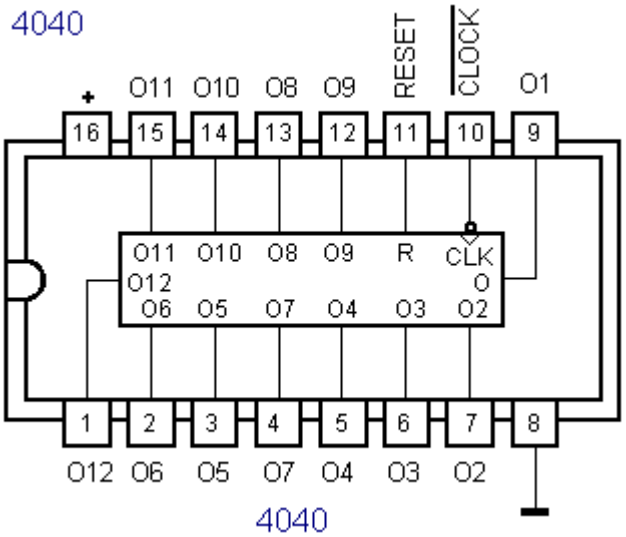
4031

4035

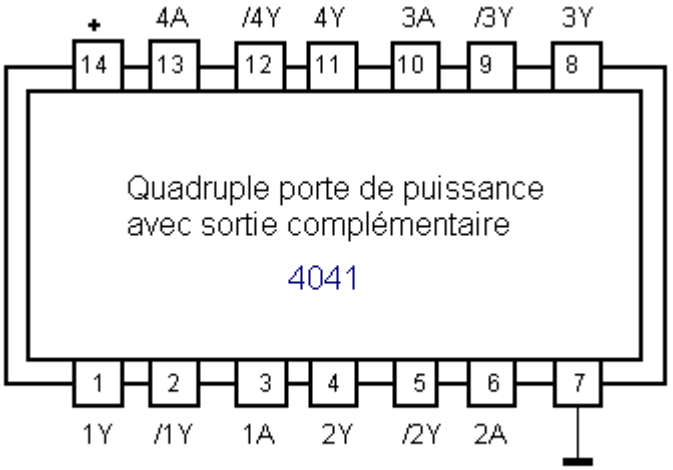


4035

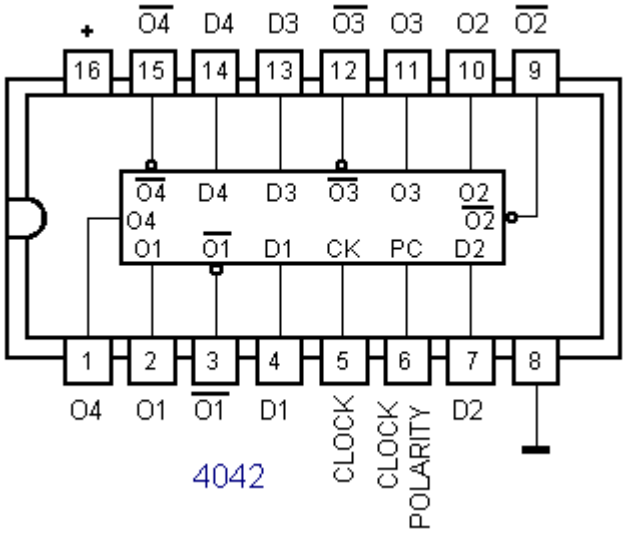
4040



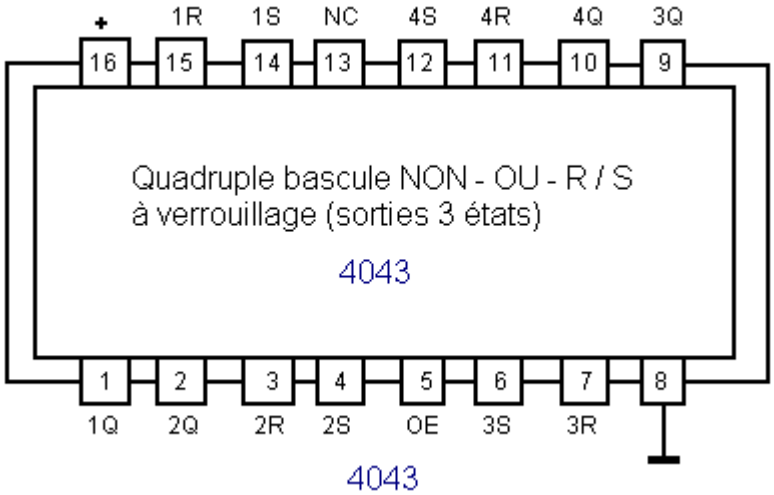
4040



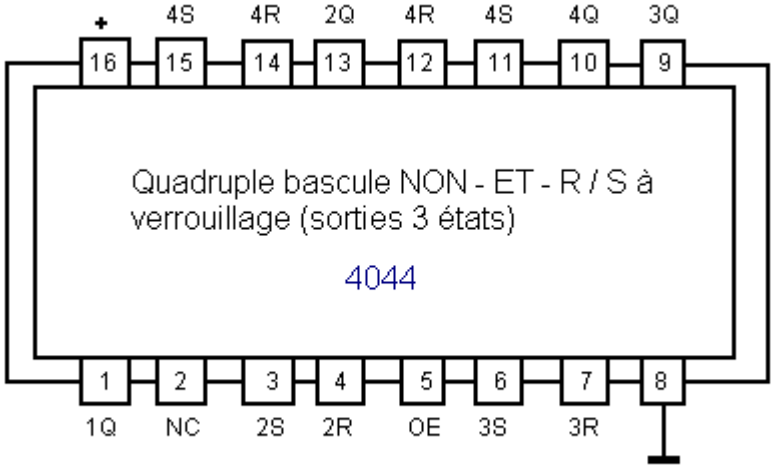
4041



4042

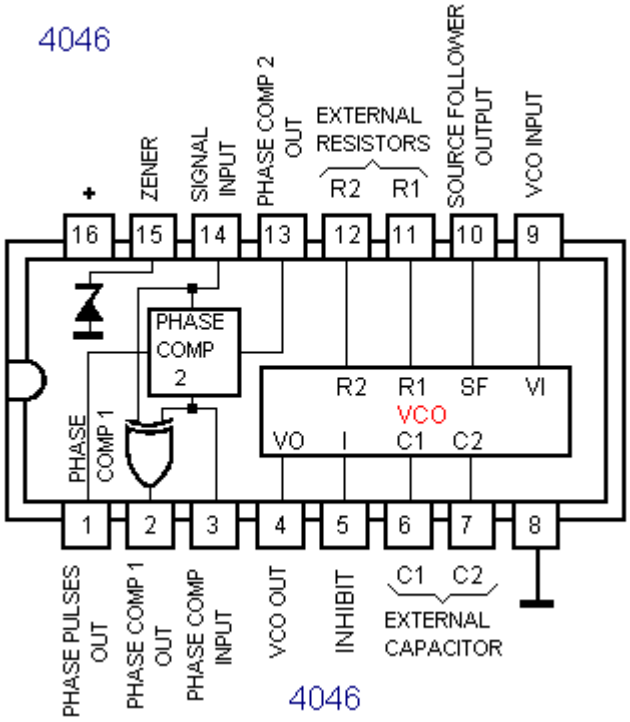


4043

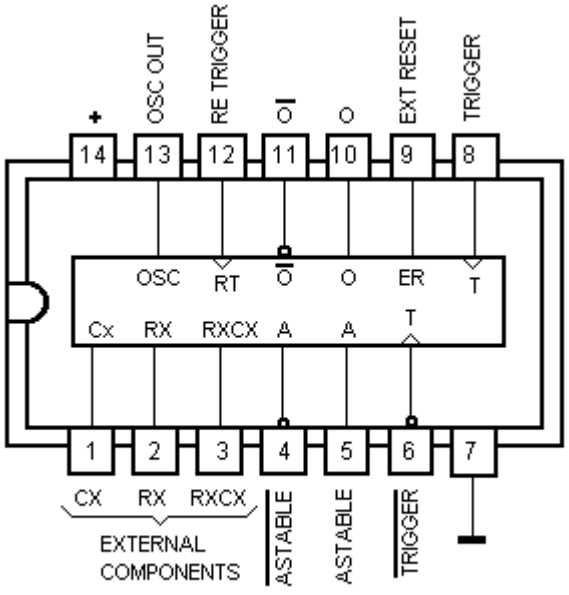


4044

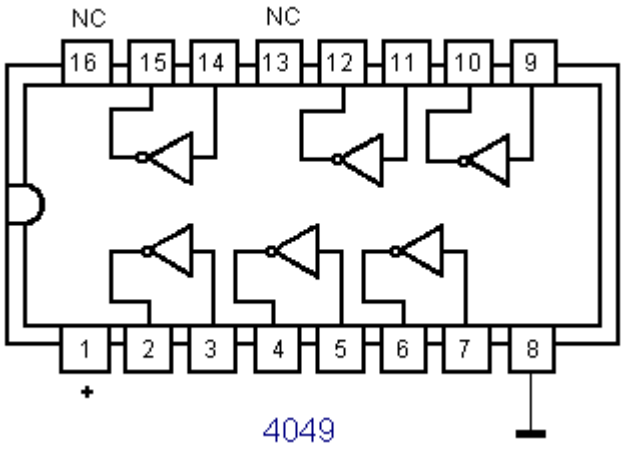
4046



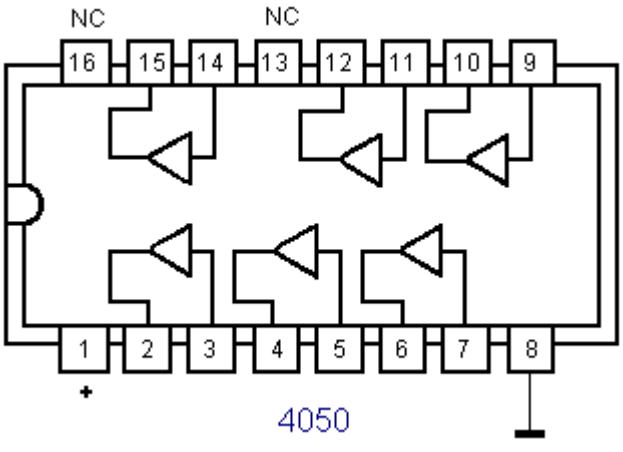
4046



4047

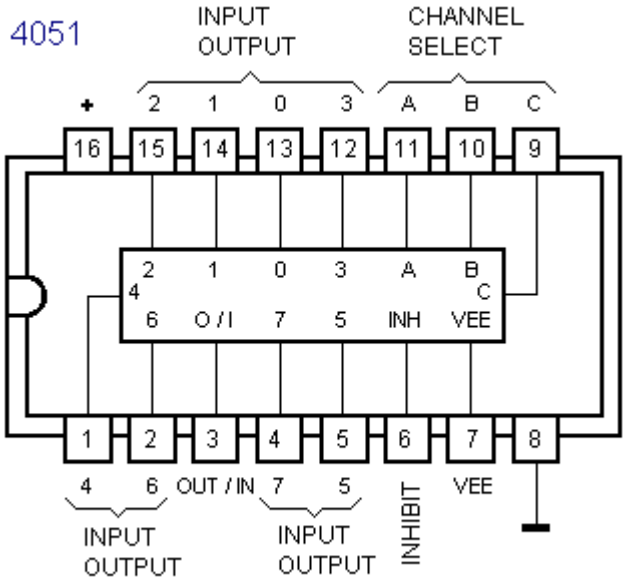


4049



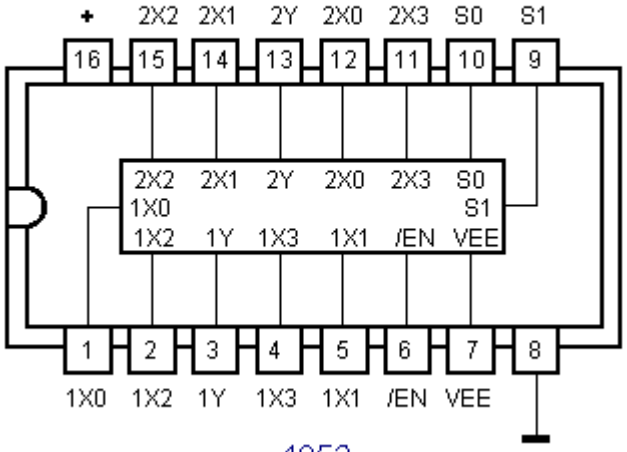
4050

4051



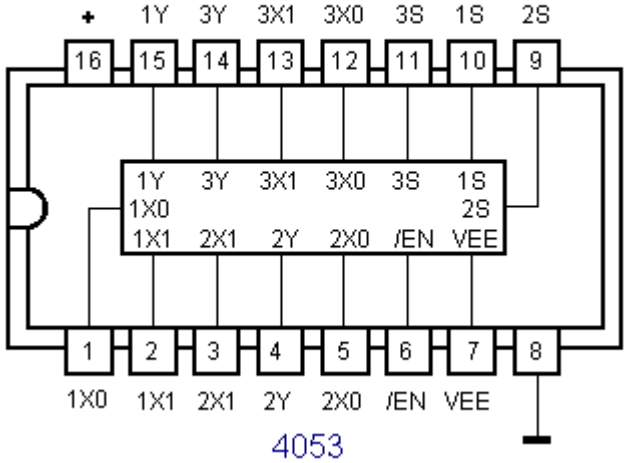
4051

Double multiplexeur / démultiplexeur analogique à 4 canaux

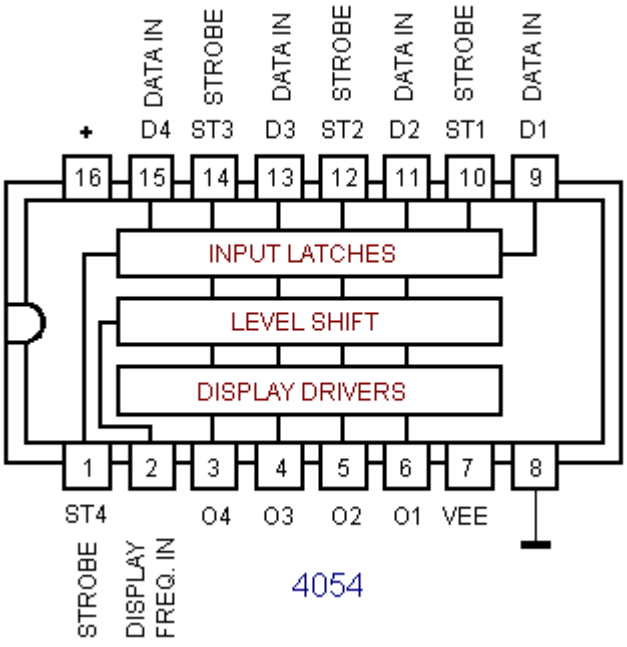


4052

Triple multiplexeur / démultiplexeur analogique à 2 canaux

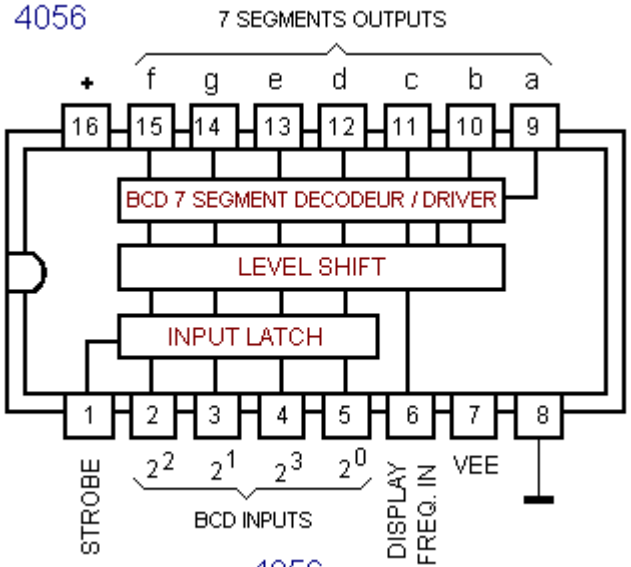


4053



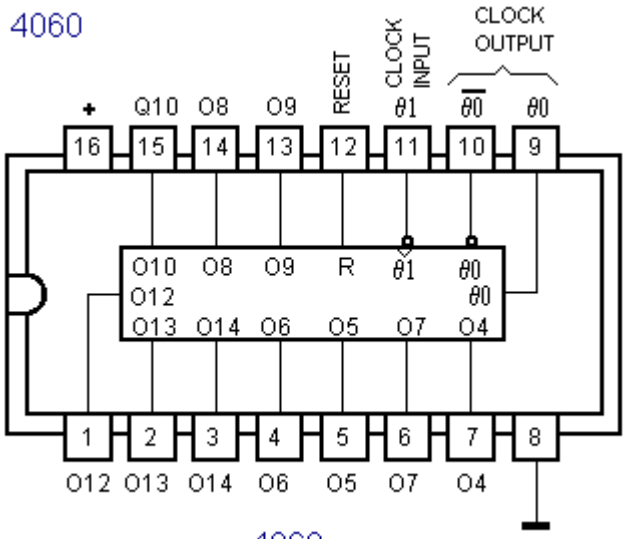
4054

4056

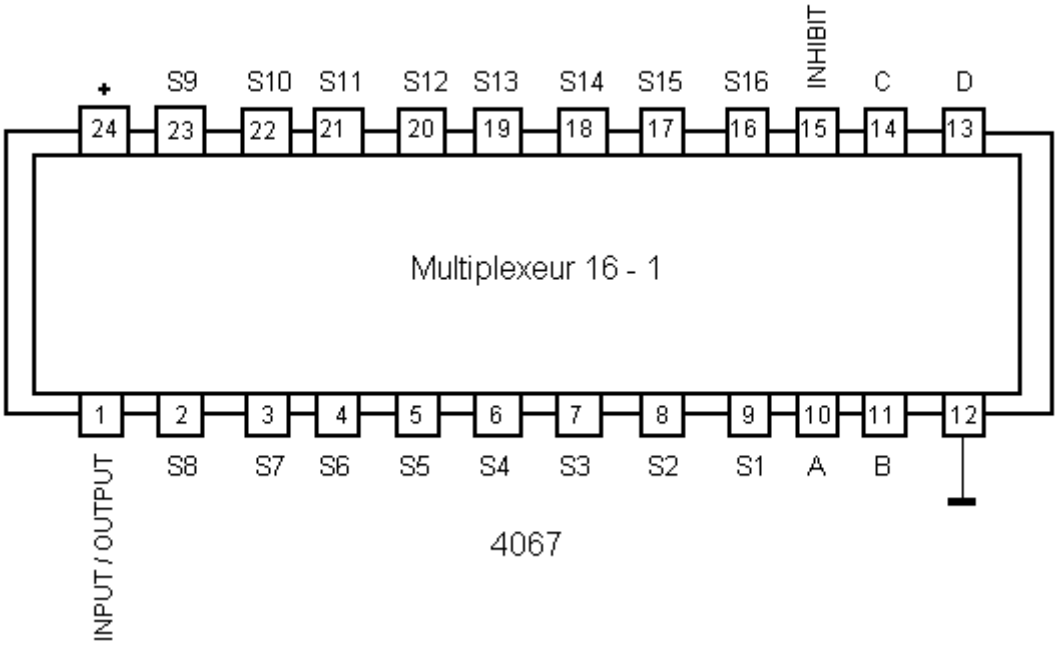


4056

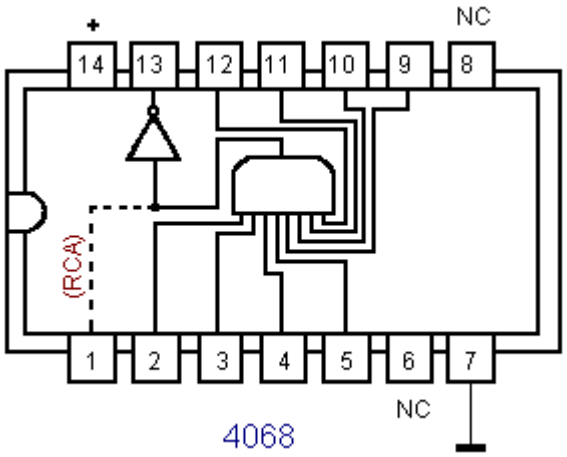
4060



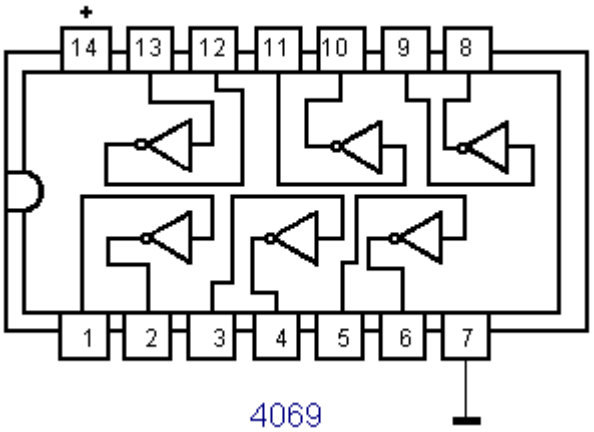
4060



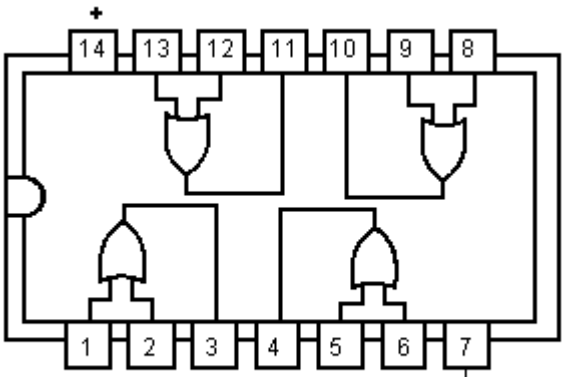
4067



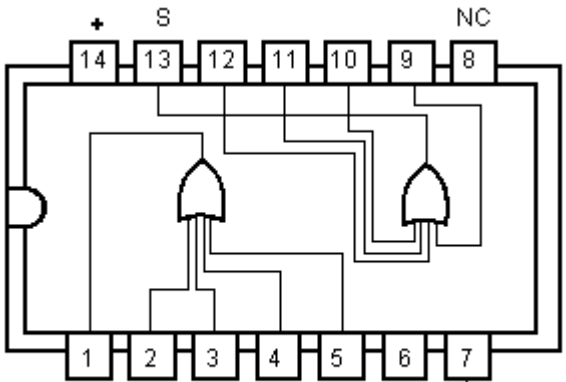
4068



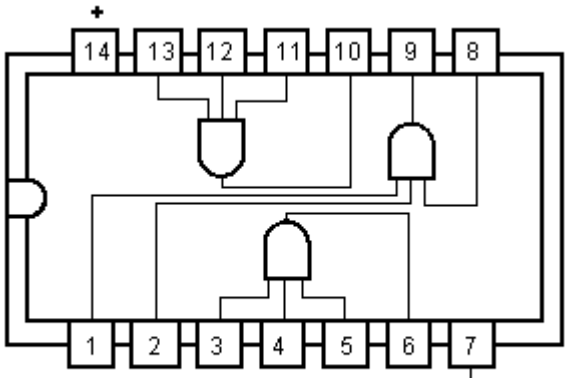
4069



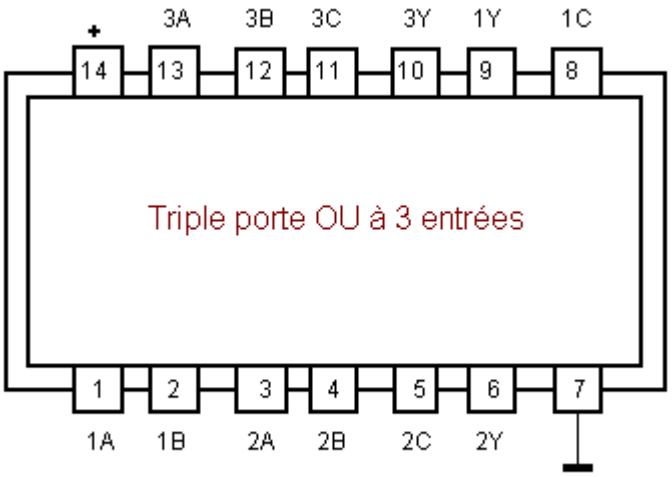
4071



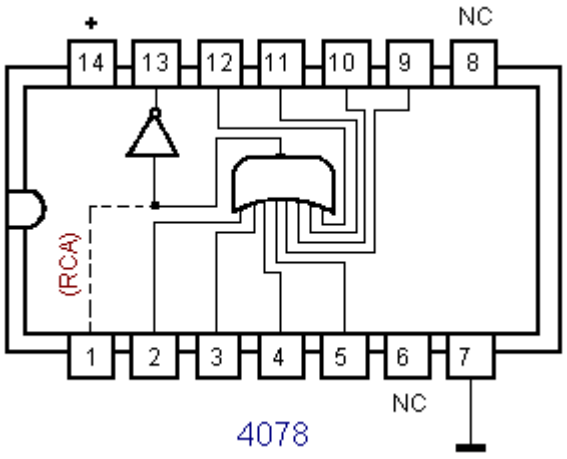
4072



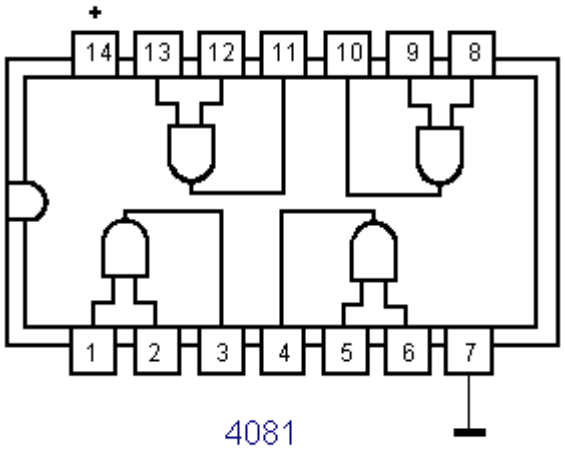
4073



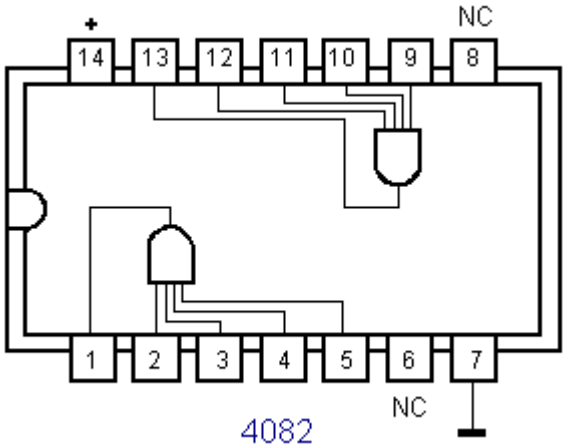
4075



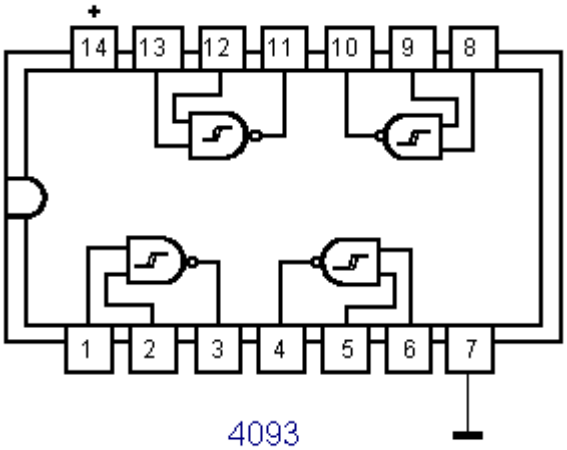
4078



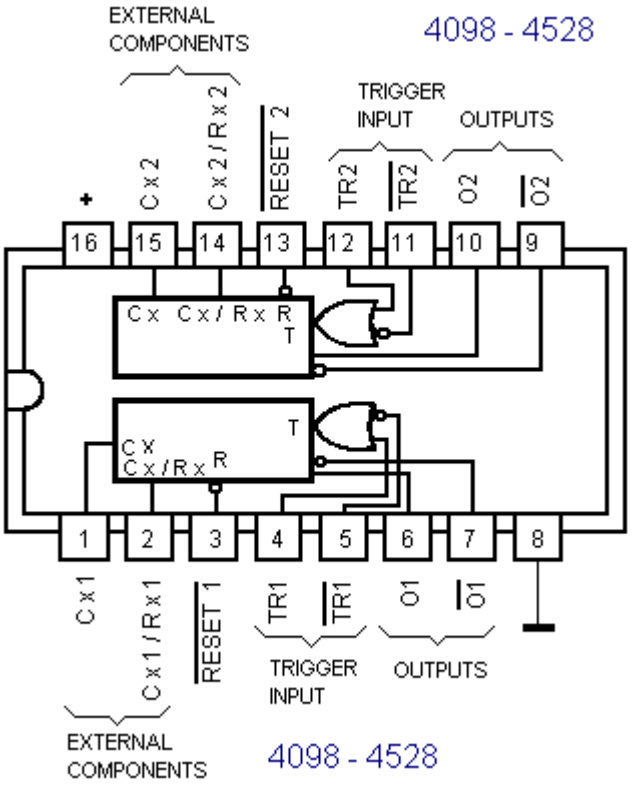
4081



4082



4093



4098 - 4528

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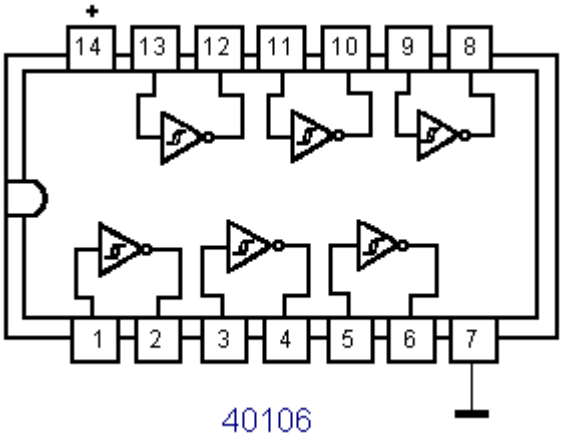
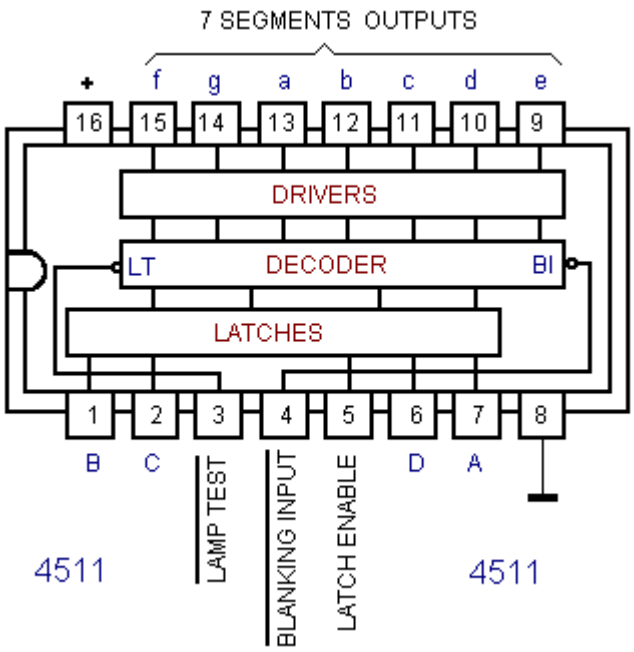
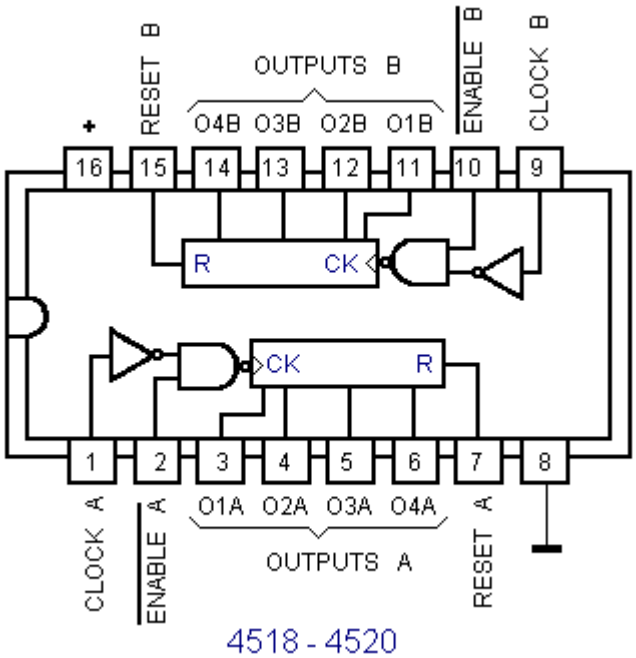


40106



4511

4511



4518 - 4520